

REMARKS

The Examiner is thanked for the thorough review and consideration of the present application. The non-final Office Action dated December 30, 2003 has been received and its contents carefully reviewed.

By this Response, Applicants have amended claims 1 and 3. No new matter has been added. Claims 1-15 are pending with claims 4-15 being withdrawn from consideration. Reconsideration and withdrawal of the rejections in view of the above amendments and the following remarks are requested.

In the Office Action, claims 1-3 are rejected under 35 U.S.C. § 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicants regard as the invention. Applicants have amended claims 1 and 3. Reconsideration and withdrawal of the rejection are requested.

In the Office Action, claims 1-3 are rejected under 35 U.S.C. § 102(e) as being anticipated by U.S. Patent No. 6,587,160, issued to Lee et al. (hereafter "Lee"). Applicants traverse the rejection because Lee fails to teach or suggest each of the features recited in the claims of the present application. For example, Lee fails to teach or suggest a liquid crystal display device that includes, among other features, "a width of an overlapping portion of the gate electrode and the drain electrode varies in accordance with a position of a respective one of the thin film transistors", as recited in independent claim 1 of the present application.

Lee discloses dummy pixel for discharging electrostatic charges. In FIG. 19 of Lee, "three (i.e., a plurality) of source electrodes 115, 116 and 117 are connected to the dummy data line 110, and dummy drain electrodes 125, 126 and 127, which respectively correspond to the dummy source electrodes 301 through contact holes C5, C6 and C7" (col. 10, lines 53-65). However, Applicants respectfully submit Lee teaches merely about the width DW between the source and drain electrodes. In particular, "the width DW1 of the first dummy source and drain electrodes 115 and 125 is narrower than the width DW2 of the second dummy source and drain electrode 116 and 126, and the width DW2 of the second dummy source and drain electrodes 116 and 126 is narrower than the width DW3 of the third dummy source and drain electrodes 117 and 127" (col. 10, line 66- col. 11, line 7). As such, Lee fails to teach or suggest a relationship between the overlap of gate electrode and drain electrode and the position of the TFT as recited in claim 1. Specifically, Lee fails to teach or suggest, "a width of an overlapping

Application No.: 09/910,058
Amendment dated March 26, 2004
Reply of non-final Office Action dated December 30, 2004

Docket No.: 8733.490.00-US

BEST AVAILABLE COPY

portion of the gate electrode and the drain electrode varies in accordance with a position of a respective one of the thin film transistors", as recited in claim 1 and its dependent claims 2-3.

Because Lee fails to teach or suggest each of the features recited in the present application, claims 1-3 are allowable over Lee. Reconsideration and withdrawal of the rejection are requested.

In view of the above, each of the presently pending claims in this application is believed to be in immediate condition for allowance. Accordingly, the Examiner is respectfully requested to pass this application to issue. If the Examiner deems that a telephone conversation would further the prosecution of this application, the Examiner is invited to call the undersigned at (202) 496-7500.

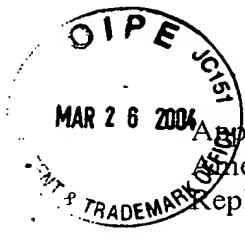
If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911. A duplicate copy of this sheet is enclosed.

Dated: March 26, 2004

Respectfully submitted,

By Valerie Hayes
Valerie Hayes

Registration No.: 53,005
MCKENNA LONG & ALDRIDGE LLP
1900 K Street, N.W.
Washington, DC 20006
(202) 496-7500
Attorney for Applicant



Application No.: 09/910,058
Amendment dated March 26, 2004
Reply of non-final Office Action dated December 30, 2004

Docket No.: 8733.490.00-US

BEST AVAILABLE COPY

portion of the gate electrode and the drain electrode varies in accordance with a position of a respective one of the thin film transistors", as recited in claim 1 and its dependent claims 2-3.

Because Lee fails to teach or suggest each of the features recited in the present application, claims 1-3 are allowable over Lee. Reconsideration and withdrawal of the rejection are requested.

In view of the above, each of the presently pending claims in this application is believed to be in immediate condition for allowance. Accordingly, the Examiner is respectfully requested to pass this application to issue. If the Examiner deems that a telephone conversation would further the prosecution of this application, the Examiner is invited to call the undersigned at (202) 496-7500.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911. A duplicate copy of this sheet is enclosed.

Dated: March 26, 2004

Respectfully submitted,

By Valerie Hayes
Valerie Hayes

Registration No.: 53,005
MCKENNA LONG & ALDRIDGE LLP
1900 K Street, N.W.
Washington, DC 20006
(202) 496-7500
Attorney for Applicant